



# AN1012 APPLICATION NOTE

## Predicting the Battery Life and Data Retention Period of NVRAMs

### INTRODUCTION

Standard SRAM devices have the advantage, over EEPROM and Flash memory, of high write-speed when used as main memory for a processor or microcontroller. Their disadvantage is that they are volatile, and lose their contents as soon as the power supply is removed (whether this is for a prolonged period due to being turned off, or due to an unexpected glitch or loss of the power supply).

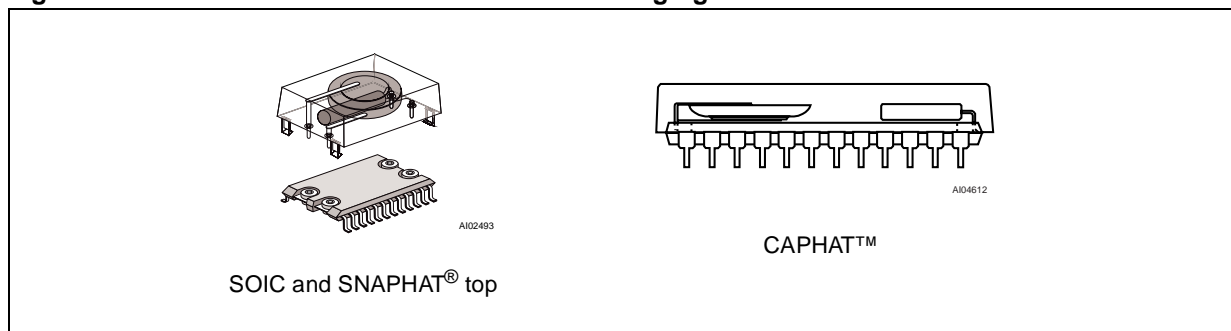
STMicroelectronics manufactures a line of non-volatile SRAMs (NVRAMs), known as ZEROPOWER<sup>®</sup> and TIMEKEEPER<sup>®</sup>, that offer the best of both worlds: memory devices that are non-volatile like EEPROM, yet with the fast access of SRAM. These devices consist of an array of low-power CMOS SRAM, plus a small long-life lithium power cell (along with a high-accuracy quartz crystal, in the case of the TIMEKEEPER). While the external power supply is within its specified limits, the memory behaves as standard SRAM; but as soon as the external power supply strays out of tolerance, the SRAM becomes write-protected, and its contents are preserved by a small trickle current supplied by the internal power cell.

Unlike EEPROM, where the data contents are guaranteed to be preserved for 10 years (and will typically last for much longer), the contents of NVRAM will only be retained while the internal cell is able to supply sufficient current to maintain the array. This document outlines the factors involved in predicting the battery life, and hence data retention, under various operating conditions.

The ZEROPOWER and TIMEKEEPER devices are each packaged in either a 600 mil DIP CAPHAT<sup>™</sup> or in a 330 mil SOIC SNAPHAT<sup>®</sup>. The SNAPHAT, shown in Figure 1, has a removable top that includes both the long life lithium cell and, in the case of the TIMEKEEPER, a high accuracy crystal.

STMicroelectronics has shipped several million SNAPHATs that have been used in a broad range of applications. From PC based systems to high-end workstations, telecommunications, consumer, and automotive applications, ZEROPOWER and TIMEKEEPER products have provided highly reliable data storage for the electronics industry.

Figure 1. ZEROPOWER<sup>®</sup> and TIMEKEEPER<sup>®</sup> Packaging



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### PROCESS TECHNOLOGY

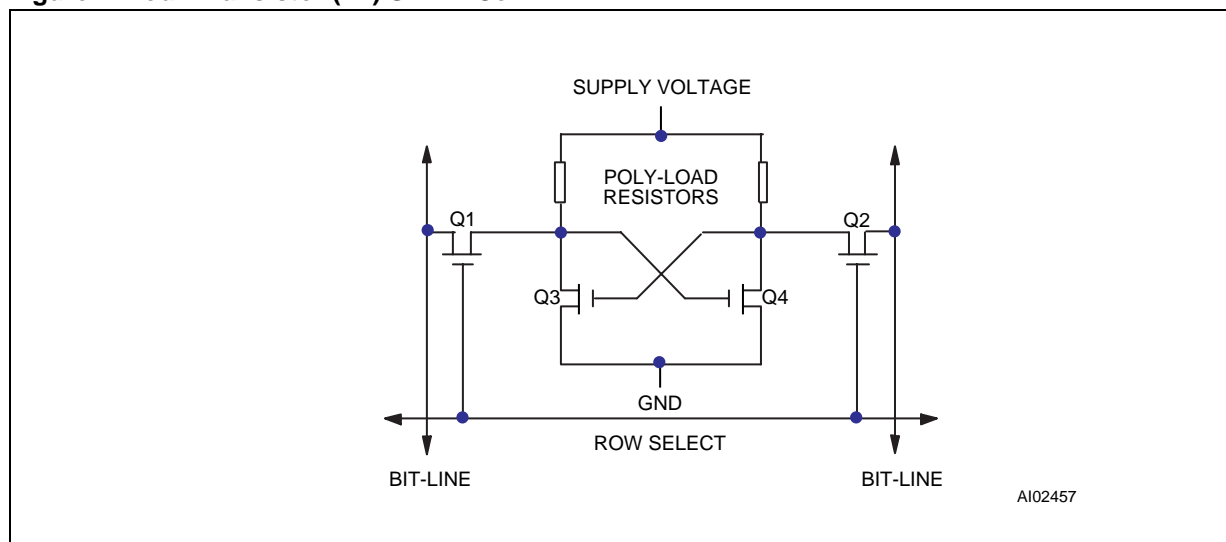
The ZEROPOWER<sup>®</sup> and TIMEKEEPER<sup>®</sup> family consists of a broad range of products that encompass various technologies. These products can be divided into eight categories, as shown in Table 1. The SRAM array is generally based on a 6-transistor cell, or a 4-transistor cell, as indicated by the references to 6T and 4T, respectively. By way of illustration, a one-bit storage cell from a 4-transistor SRAM cell is shown in Figure 2.

The Hybrid devices (also known as Module devices) contain individually packaged analog circuitry and SRAM. They are not covered in this document, except for the table of values for typical battery lifetimes that has been included, for reference, in Appendix A.

**Table 1. ZEROPOWER and TIMEKEEPER<sup>®</sup> Product Categories**

Category	Devices
ZEROPOWER (6T cell)	M48Z08, M48Z18
ZEROPOWER (4T cell)	M48Z02, M48Z12, M48Z58, M48Z58Y, M48Z35, M48Z35Y
ZEROPOWER Hybrid	M48Z128, M48Z128Y, M48Z129Y, M48Z129V, M48Z512A, M48Z512AY, M48Z2M1, M48Z2M1Y
TIMEKEEPER (6T cell)	M48T08, M48T18, M48T86
TIMEKEEPER (4T cell)	M48T02, M48T12, M48T58, M48T58Y, M48T59, M48T59Y, M48T59V, M48T59Y, M48T35, M48T35Y, M48T37Y, M48T37V
TIMEKEEPER Hybrid	M48T128Y, M48T128V, M48T129Y, M48T129V, M48T512Y, M48T512V, M48T513Y, M48T513V
SUPERVISORS	M40Z111, M40Z111W, M40Z300, M40Z300W, M48T201Y, M48T201V, M48T212Y, M48T212V
Serial RTCs (6T cell)	M41T56, M41T11, M41T00, M41T81, M41T94, M41T315, M41ST84Y, M41ST84W, M41ST85Y, M41ST85W

**Figure 2. Four-Transistor (4T) SRAM Cell**



The first devices, released in 1982, were based on a conventional 6T, full-CMOS, SRAM design. These were specified for low-voltage data retention, and were built to stringent manufacturing and test specifications. With data retention currents of less than 150 nA at 70 °C, these devices were designed to retain data in battery back-up for at least 10 years over the full commercial temperature range.

Newer devices have since been released. They use 4T, CMOS SRAM arrays. By using two poly-R resistors in place of the pull-up transistors of full-CMOS design, the 4T cell is much smaller than the 6T equivalent. Die size is dramatically reduced because the poly-R resistors can be stacked on top of n-channel pull-down MOSFETs in the cell. This leads to a net reduction in the device costs. Although the current drawn from the lithium cell is increased, the devices have been specified to outlast the useful life of most equipment in which they are used.

**BATTERY TECHNOLOGY**

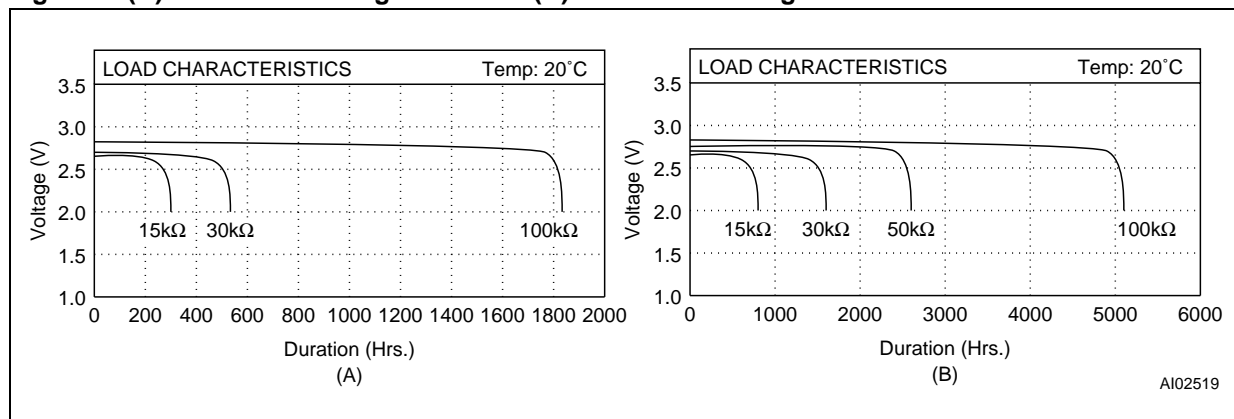
STMicroelectronics uses both, the BR1225 and the BR1632 lithium button cell batteries. These have charge capacities of 48 mAh and 120 mAh, respectively. Their constituents have non-toxic and non-corrosive characteristics, and are chemically and thermally stable before, during, and after discharge. This makes these cells particularly attractive for use in electrical components.

They contain a solid carbon cathode that is pressed into a tablet of predetermined weight and height. The anode consists of high-purity lithium metal. The electrolyte is based on an organic solvent, instead of the corrosive alkaline or acidic solution found in most conventional batteries. This greatly reduces the likelihood of internally-induced cell leakage, and reduces the ill effects in cases of externally-induced cell leakage. The cell is then crimp-sealed with a polypropylene grommet.

ST has conducted extensive tests on these cells, at temperatures up to 85 °C. Destructive analysis was conducted, post stress, in order to measure such factors as weight loss and remaining charge capacity. Analysis determined that the cells were drying out, and that the weight loss was due to electrolyte evaporation. Models were developed to predict the nominal rate of electrolyte loss, and how this would be reduced by adding a second level of encapsulation. This proprietary secondary seal encapsulation, adopted by ST, has been found to provide up to a two-fold reduction of the electrolyte loss rate.

As can be seen, in Figure 3, both cells produce a nominal 2.9V output with a flat discharge curve until the end of their effective lives, and so confirms that both are suitable for providing battery backup to low leakage CMOS SRAMs.

**Figure 3. (A) BR1225 Discharge Rate and (B) BR1632 Discharge Rate**



**BATTERY BACK-UP CURRENT — PREDICTING THE DATA RETENTION TIME**

A ZEROPOWER® or TIMEKEEPER® device will reach the end of its useful life for one of two reasons:

- either it will have become discharged, having provided current to the SRAM (and to the oscillator in the case of the TIMEKEEPER) in the battery back-up mode
- or else the effects of aging will have rendered the cell inoperative before the stored charge has been fully consumed by the application.

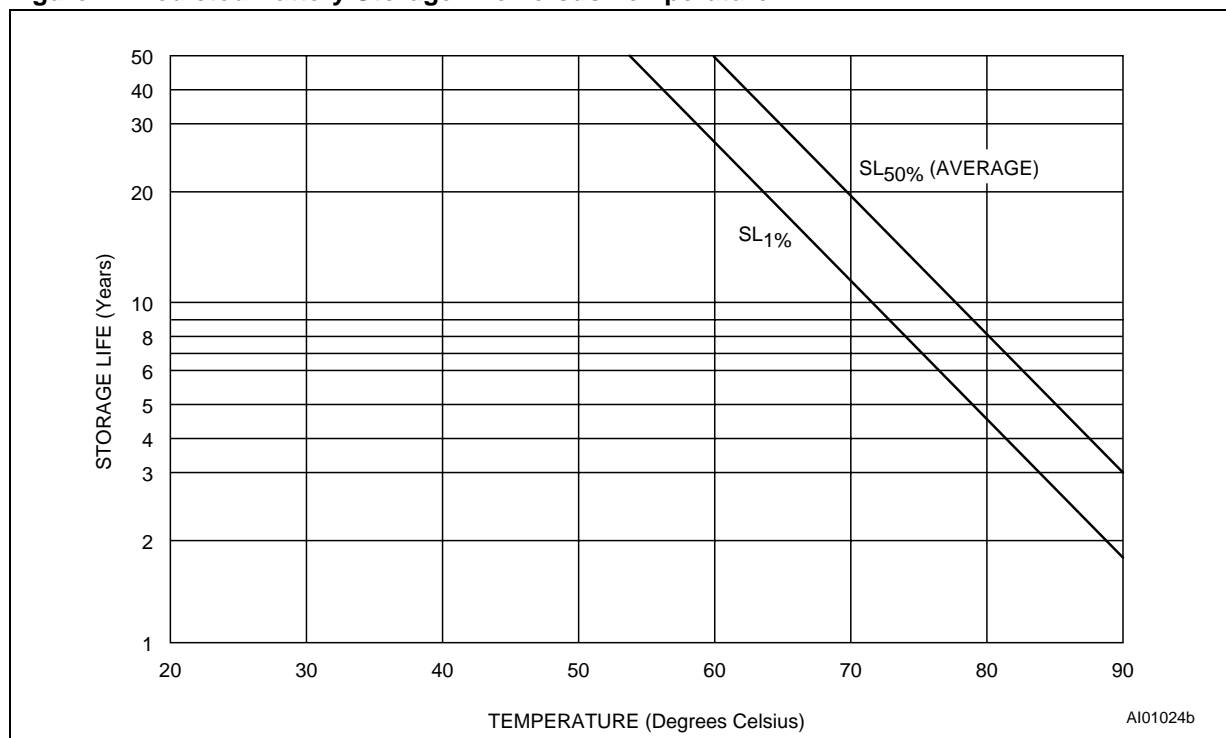
The first effect is known as Capacity Consumption, and the second is referred to as Storage Life. The two effects have very little influence on each other, allowing them to be treated as two independent but simultaneous mechanisms. The data retention lifetime of the device is determined by which ever failure mechanism occurs first.

**Storage Life**

Storage life, resulting from electrolyte evaporation, is primarily a function of temperature. Figure 4 illustrates the predicted storage life of the BR1225 battery versus temperature. The results are derived from temperature accelerated life test studies performed at STMicroelectronics. For the purpose of testing, a cell failure is defined as the inability of a cell, stabilized at 25°C, to produce a 2.4V closed-circuit voltage across a 250 kΩ load resistor. The two lines, SL<sub>1%</sub> and SL<sub>50%</sub>, represent different failure rate distributions for the cell's storage life. At 60 °C, for example, the SL<sub>1%</sub> line indicates that the battery has a 1% chance of failure 28 years into its life, and the SL<sub>50%</sub> line shows that the battery has a 50% chance of failure at the 50 year mark. The SL<sub>1%</sub> line represents the practical onset of wear out, and can be considered the worst case Storage Life for the cell. The SL<sub>50%</sub> line can be considered to be the normal, or average, life. As indicated by the curves in Figure 4, Storage Life does not become a limiting factor to overall Battery Life until temperatures in excess of 60 °C to 70 °C are involved.

As an approximation,  $SL_{50\%} = 14270 \times (0.91)^T$ , and  $SL_{1\%} = 8107 \times (0.91)^T$ , when 20°C < T < 90°C.

**Figure 4. Predicted Battery Storage Life versus Temperature**



### Calculating Storage Life

Because the ambient temperature profile is dependent upon application controlled variables, only the user can estimate predicted Storage Life in a given design. As long as the ambient temperature is held reasonably constant, the expected Storage Life can be read directly from Figure 4. If the battery spends an appreciable amount of time at a variety of temperatures, the following formula can be used to estimate predicted Storage Life:

$$\left[ \left( \frac{t_1}{T} \times \frac{1}{SL_1} \right) + \left( \frac{t_2}{T} \times \frac{1}{SL_2} \right) + \dots + \left( \frac{t_n}{T} \times \frac{1}{SL_n} \right) \right]^{-1}$$

where,

- $t_i/T$  is the relative proportion (of the total time) during which the device is at ambient temperature  $TA_i$
- $SL_i$  is the storage life at ambient temperature  $TA_i$  as found from Figure 4
- $T$  is the total time =  $t_1 + t_2 + \dots + t_n$

For example, consider a battery exposed to temperatures of up to 90°C for 600 hrs/yr, and temperatures of 60°C or less for the remaining 8160 hrs/yr. Reading predicted  $t_{1\%}$  values from Figure 4,

- $SL_1$  is about 1.8 yrs
- $SL_2$  is about 28 yrs
- $T$  is 8760 hrs/yr
- $t_1$  is 600 hrs/yr
- $t_2$  is 8160 hrs/yr

The predicted storage life evaluates to:

$$\left[ \left( \frac{600}{8760} \times \frac{1}{1.8} \right) + \left( \frac{8160}{8760} \times \frac{1}{28} \right) \right]^{-1}$$

This predicts that the storage life, in this particular case, is at least 14 years. This is, therefore, better than the normally accepted life time of 10 years.

### Capacity Consumption

When  $V_{CC}$  is being held by the external power supply within its specified range, the current drawn from the battery is zero. When  $V_{CC}$  falls below the Battery Back-up Switch-over Voltage ( $V_{SO}$ ), the device goes into battery back-up mode, and draws all its current from the battery.

The  $V_{CC}$  duty cycle represents the proportion of time, expressed as a percentage, that the device is supplied with power from the external supply, and hence not drawing current from the battery.

In its battery backup mode, the array of SRAM cells can be characterized by its data retention ( $I_{CCDR}$ ) current, caused primarily by the current through the Poly-R load resistors in the 4T technology, and also by junction leakage, sub-threshold current, and gate-to-substrate leakage. The total current is referred to as  $I_{BAT}$  (the current drawn during battery back-up mode). For ZEROPOWER devices, this is the sum of leakage currents plus the current necessary to maintain the SRAM array. For TIMEKEEPER devices, it is the sum of the array current (including leakage) and the clock current:

$$I_{BAT} = I_{ARRAY} + I_{CLOCK}$$

Many factors need to be taken into account when calculating the  $I_{BAT}$  current, including process parameters, working temperature and the  $V_{CC}$  duty cycle.

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### Calculating Capacity Consumption

Capacity consumption is simply calculated by:

$$\frac{\text{BatteryCapacity}}{8760 \times (1 - V_{CC} \text{DutyCycle}/100) \times I_{BAT}}$$

where:

- Battery Capacity is measured in ampere-hours
- 8760 is the constant for the number of hours there are in a year
- $V_{CC}$  Duty Cycle is measured as a percentage
- $I_{BAT}$  is measured in amperes.

For the M48T35Y, a 32K x 8 TIMEKEEPER® device, with a 0.048Ah (48 mAh) M4T28-BR12SH1 battery, the typical battery current is approximately 2666nA at 70°C.

Thus, if the  $V_{CC}$  Duty Cycle is 50%, the predicted capacity life is:

$$\frac{0.048}{8760 \times 0.5 \times 2666 \times 10^{-9}}$$

and hence is about 4.11 years at 70°C.

### 6T CELL DEVICES

As can be seen from Appendix A, the M48Z08 and M48Z18 ZEROPOWER® array uses the 6T technology, and requires fairly insignificant battery currents (about 23nA at 25°C).

Appendix B shows that even if the device is kept constantly in the battery back-up mode (at a  $V_{CC}$  duty cycle of 0%) and at a temperature of 60°C, the battery life can be expected to be greater than 20 years.

### 4T CELL DEVICES

In moving to the newer process technologies, for example, the M48Z58 (8K x 8) device, STMicroelectronics has chosen to reduce the active current as well as decrease the die size. The STMicroelectronics HCMOS4PZ process is a 0.6  $\mu\text{m}$ , double-level metal process.

In the standard SRAM memory cell, 6 transistors are formed into a pair of cross-coupled inverters. In the 4T memory cell, the top two P-channel devices are replaced by poly-silicon load resistors (poly-R). This combination allows for significant die size reductions because the poly-R structures can be stacked on top of the active N-channel devices.

There is always at least one direct path constantly leaking current to ground in each cell because of the poly-R structures in each SRAM cell. However, the value of the resistor is extremely high (about 3 T $\Omega$  at 25°C), so at a cell voltage of 3V, this leads to a leakage current of 1pA. Multiplying by the number of cells within the array, the array stand-by current can be calculated (i.e. 65.5nA for a 65536-cell array).

The poly-R structure values are dependent on temperature, so the entire array current is very strongly temperature-dependent. Appendix B shows the expected battery lifetime of an M48Z58 device versus working temperature, at a  $V_{CC}$  duty cycle of 0%.

The original specification was an expected lifetime of greater than 10 years at 25°C but, in fact, this target is typically achieved even at 70°C. By reducing the temperature, the expected lifetime rises to greater than 20 years when the device is operated at 50°C. This change is defined entirely by the temperature sensitivity of the poly-R structures within each SRAM cell.

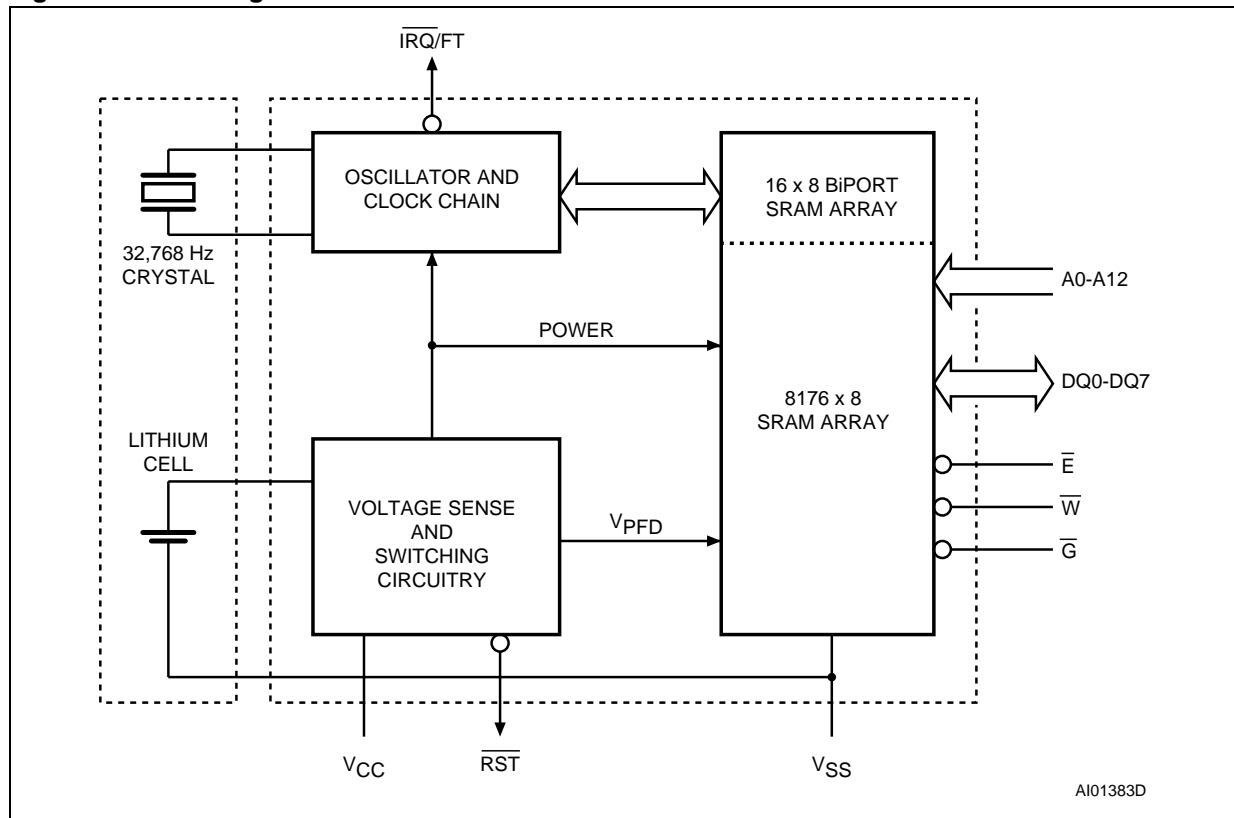
The M48Z35 also employs the STMicroelectronics HCMOS4PZ process, 4T SRAM cell technology. Appendix B shows the expected battery lifetime of an M48Z35 device versus working temperature, at a  $V_{CC}$  duty cycle of 0%. From this we can see that expected lifetime is typically greater than 20 years when operated at 30°C with no external  $V_{CC}$  applied, and degrades to approximately 2.7 years for continuous battery back-up at 70°C. As before, this is to be expected, due to the increased current consumption inherent in the 4T SRAM cell architecture. It should be noted that this data is based on usage of the SNAPHAT product which includes a 48mAh battery.

**TIMEKEEPER PRODUCTS**

TIMEKEEPER® products are very similar in construction and operation to ZEROPOWER® products. However, they must be evaluated separately. The current drawn is highly dependent not only on the temperature, but also on whether the oscillator is active. The main components of TIMEKEEPER devices, as shown in Figure 5, are:

- a CMOS RAM array
- voltage sense and switching circuitry
- an analog oscillator and clock chain
- a lithium power cell
- a high accuracy quartz crystal

**Figure 5. Block Diagram of a TIMEKEEPER® Device**



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### TIMEKEEPER® Register Map

Table 2 shows a typical register map for the seconds, minutes, hours, date, day, month, and year fields. This information is stored in BCD format. These basic functions are available on all TIMEKEEPER® devices. Additional features (such as a watchdog timer, alarms, a battery low flag and a wake-up function) have additional registers allocated to them (such as those shown for the M48T37Y in Table 2). The TIMEKEEPER register locations are constructed from BiPORT™ memory cells which allow data access from two sides. The on-chip system clock connects to one side (the system side) and the user data is output connects to the other (the user side). At one second intervals, clock pulses are generated by the oscillator and clock chain structure. The system side updates the new time in the TIMEKEEPER registers. Each TIMEKEEPER register location (e.g. minutes, hours, day) is then updated as necessary. When the user wants to write a new time, the “W” bit (the Write Bit) of the Control Register is reset, causing the BiPORT cells to upload the new system time. The user accesses the TIMEKEEPER and array data by executing standard read/write cycles.

The oscillator and clock chain structure consists of a mixture of analog and digital circuitry, and account for the majority of the I<sub>BAT</sub> current. Table 3 gives conservative estimates of the currents drawn as a function of technology and working temperature.

**Table 2. Typical TIMEKEEPER® (M48T37Y) Register Map**

Address	Data								Function	Range (in BCD Format)
	D7	D6	D5	D4	D3	D2	D1	D0		
7FFFh	10 Years				Year				Year	00-99
7FFEh	0	0	0	10M	Month				Month	01-12
7FFDh	0	0	10 Date		Date				Date	01-31
7FFCh	0	FT	0	0	0	Day			Day	01-7
7FFBh	0	0	10 Hours		Hours				Hours	00-23
7FFAh	0	10 Minutes			10 Minutes				Minute	00-59
7FF9h	ST	10 Seconds			Seconds				Second	00-59
7FF8h	W	R	S	Calibration					Control	
7FF7h	WDS	BMB4	BMB3	BMB2	BMB1	BMB0	RB1	RB0	Watch	
7FF6h	AFE	0	ABE	0	0	0	0	0	Interrupt	
7FF5h	RPT4	0	AI 10 Date		Alarm Date				A Date	01-31
7FF4h	RPT3	0	AI 10 Hour		Alarm Hour				A Hour	00-23
7FF3h	RPT2	Alarm 10 Minutes			Alarm Minutes				A Minute	00-59
7FF2h	RPT1	Alarm 10 Seconds			Alarm Seconds				A Second	00-59
7FF1h	1000 Years				100 Years				Century	00-99
7FF0h	WDF	AF	0	BL	Z	Z	Z	Z	Flags	

**Table 3. Typical I<sub>BAT</sub> Current for TIMEKEEPER® Devices**

Capacity	Technology	Typical at 20°C		Typical at 70°C	
		Array	Clock	Array	Clock
64 Kbit	6T Cell	20nA	450nA	55nA	350nA
64 Kbit	4T Cell	40nA	497nA	511nA	619nA



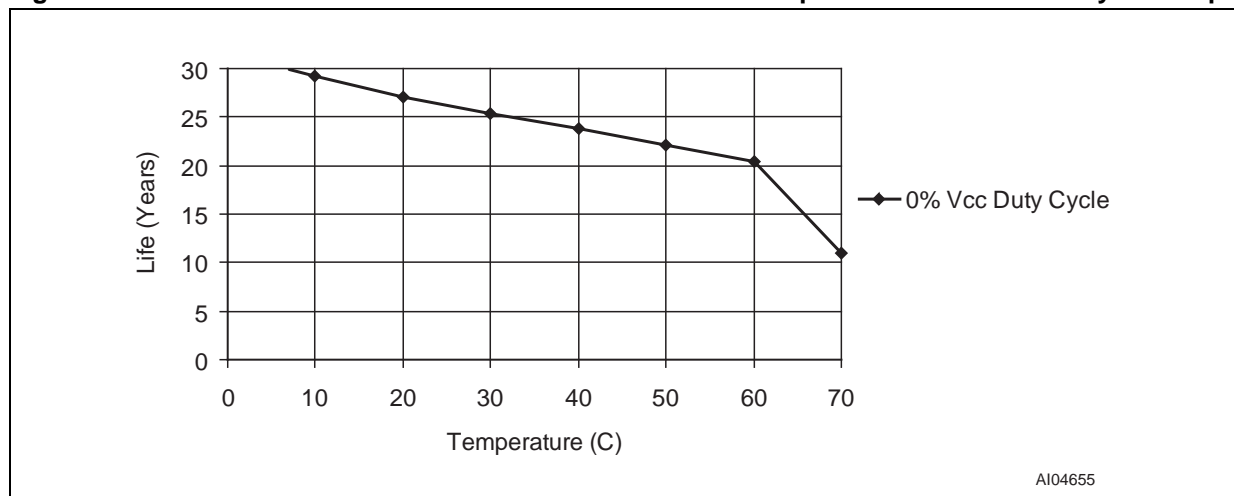
**TIMEKEEPER Evolution**

TIMEKEEPER® products have seen a continuous evolutionary cycle since their initial market introduction nearly 17 years ago.

**M48T02 and M48T12.** The first TIMEKEEPER products released were the MK48T02 and MK48T12 which offered 2K x 8 RAM and employed the STMicroelectronics 2.0  $\mu\text{m}$  Spectrum™ CMOS technology. When released, these products included a BR1225 lithium cell with a specified 39mAh capacity. This combination offered the user approximately 3.5 years of continuous battery back-up life. Since that time, the devices have been moved to the 4T cell technology (HCMOS4PZ) and a CAPHAT™-package revision was released, with a larger capacity lithium cell, the 120mAh capacity BR1632, and a new part number (M48T02 and M48T12). This increased expected battery life to greater than 20 years, even at 60°C.

In Figure 6, the expected battery lifetime data has been plotted against temperature and  $V_{CC}$  duty cycle. This data shows that by operating the devices at various temperatures, designers can expect a battery life approaching 20 years under most conditions.

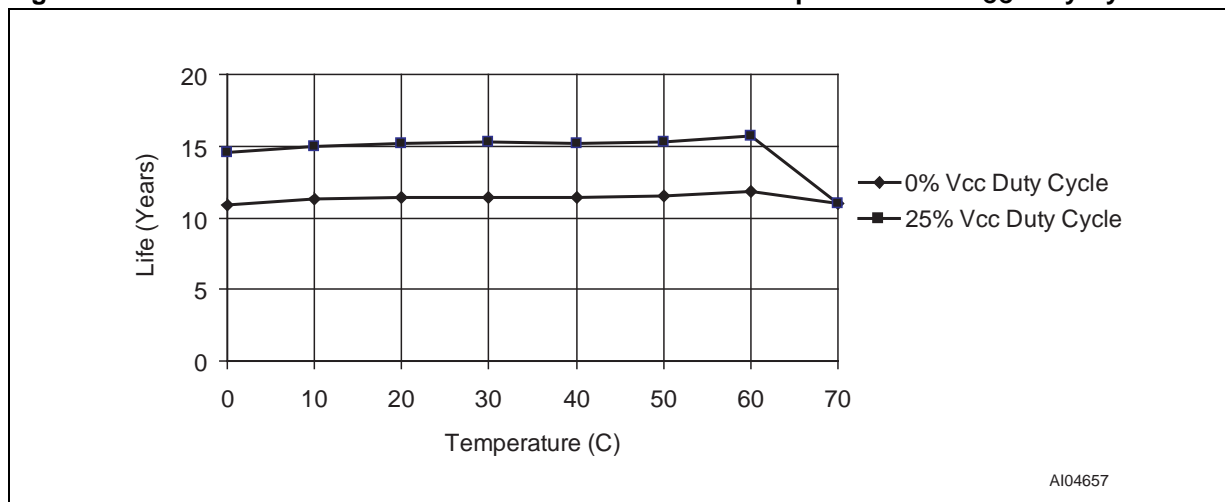
**Figure 6. M48T02 and M48T12 Data Retention Lifetime Vs. Temperature in 100% Battery Back-up**



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**M48T08 and M48T18.** The next TIMEKEEPER® to be released was the MK48T08/18 family, which has an 8K x 8 SRAM array. By using the more advanced 1.2 μm HCMOS3 process and refining the on-board oscillator, STMicroelectronics was able to offer a nearly three-fold increase in battery lifetime, even though the array size had increased by a factor of four. This product was also initially released with a 39mAh battery although this has since been upgraded to 48mAh for the CAPHAT™ package revision (part numbers M48T08/18). This gives a battery life of at least 10 years across the full temperature range (see Figure 7). In the M48T08 and M48T18 data sheet, the battery lifetime ( $t_{DR}$ , data retention time) has been specified as 10 years or greater across the temperature range 0 to 70°C (for a 0%  $V_{CC}$  duty cycle).

**Figure 7. M48T08 and M48T18 Data Retention Lifetime vs. Temperature and  $V_{CC}$  Duty Cycle**

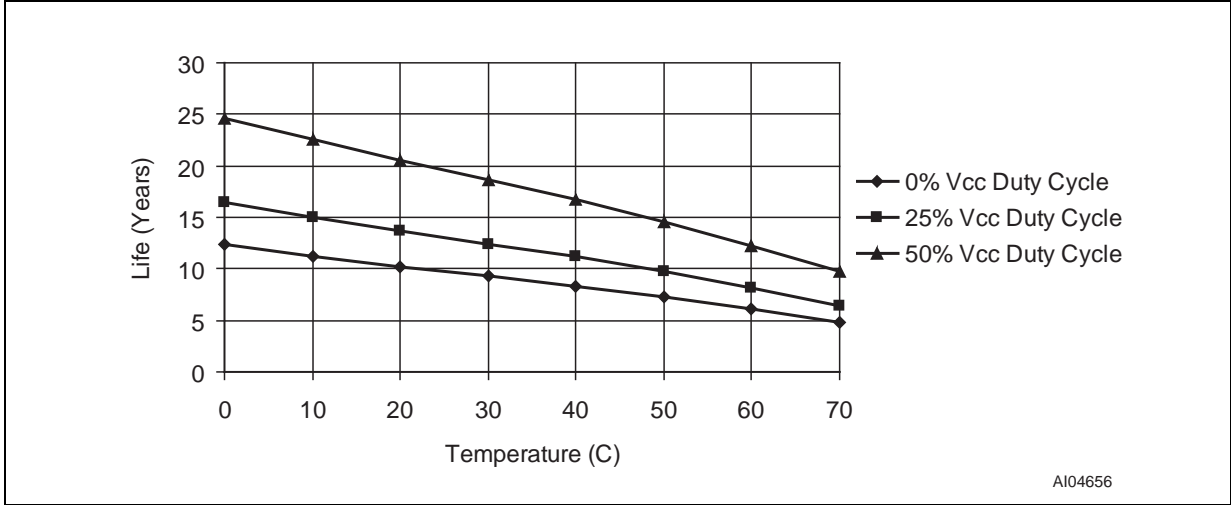


**M48T58, M48T59, and M48T559.** The next TIMEKEEPER® products were the M48T58, M48T59 and M48T559Y. These are fabricated on the 0.6 μm, double-level metal, HCMOS4PZ process for 4T SRAM cells. The M48T59 and M48T559Y offer additional features such as watchdog timers, programmable alarms, and alarm resets.

Table 19, page 23, Appendix C, and the curve of Figure 8 show the extent to which the data retention of these devices is more dependent on temperature. Because of the negative temperature coefficient of the poly-R resistors, higher temperatures cause lower resistor values, and thus higher currents.

As can be seen, data retention lifetimes typically range from 9.3 years (at 30°C) for devices in the CAPHAT™ package with a 48mAh battery, up to 20 years (and more) for the SNAPHAT package with a 120mAh BR1632 battery. As always, several factors govern the battery lifetime, including the V<sub>CC</sub> duty cycle and the temperature.

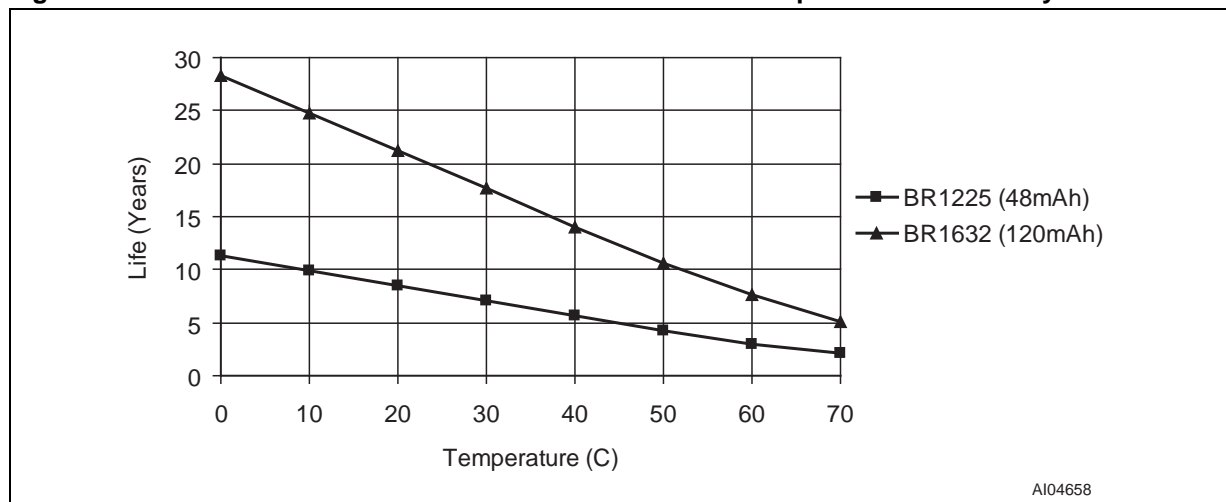
**Figure 8. M48T58, M48T59 and M48T559 Data Retention Lifetime vs. Temperature & V<sub>CC</sub> Duty Cycle**



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**M48T35 and M48T37.** The M48T35 and M48T37 families use the same technology as the M48T58, M48T59, and M48T559 devices, but with a 32K x 8 SRAM array. Figure 9 shows the battery lifetime data plotted against temperature and battery size. The expected battery lifetime (at 30°C with no periods of valid  $V_{CC}$ ) is typically 7.1 years; again, this assumes that a 48mAh battery is used. Devices in the larger M4T32-BR12SH SNAPHAT® package have a data retention lifetime of greater than twice this (almost 17 years).

**Figure 9. M48T35 and M48T37 Data Retention Lifetime vs. Temperature and Battery Size**



If data retention lifetimes greater than those shown are required, the user is advised to choose the version of the device in a SNAPHAT® package. Then, as the battery starts to reach the end of its useful life, it is possible to remove the SNAPHAT top containing the nearly expended cell, and to replace it with a fresh SNAPHAT top. No data will be lost during the process, provided that the board remains powered-up during the operation (although some time will be lost due to the momentary removal of the 32 kHz crystal). Table 4 shows which SNAPHAT top part numbers are available.

**Table 4. SNAPHAT Part Numbers**

Part Number	Description	Package
M4Z28-BR00SH	Li Battery (48mAh) for ZEROPOWER products and SUPERVISORS	SNAPHAT
M4Z32-BR00SH	Li Battery (120mAh) for ZEROPOWER products and SUPERVISORS	SNAPHAT
M4T28-BR12SH	Li Battery (48mAh) for TIMEKEEPER products and SUPERVISORS	SNAPHAT
M4T32-BR12SH	Li Battery (120mAh) for TIMEKEEPER products and SUPERVISORS	SNAPHAT

## SUPERVISOR PRODUCTS

STMicroelectronics recently introduced a family of ZEROPOWER<sup>®</sup> and TIMEKEEPER<sup>®</sup> SUPERVISOR devices. The SUPERVISOR is a self-contained unit that allows standard low-power SRAMs to be turned into non-volatile memory devices. In the same way that the ZEROPOWER and TIMEKEEPER products use a precision voltage reference and comparator to monitor the  $V_{CC}$  input for going out-of-tolerance, the SUPERVISOR device monitors, and provides the  $V_{CC}$  input for one or more external SRAMs. When  $V_{CC}$  becomes invalid, the SUPERVISOR's conditioned chip-enable outputs ( $\overline{E}_{CON}$ ) are forced to their "inactive" state, thereby putting each external SRAM into its own write-protect state. During the power failure, the SUPERVISOR provides the power for the SRAM from the lithium cell within its SNAPHAT top. The SUPERVISOR switches the power source back to the  $V_{CC}$  supply as soon as the voltage level returns to within specification.

## CHOOSING SRAM

Most low power SRAMs on the market today can be used with both ZEROPOWER and TIMEKEEPER SUPERVISORS, although there are some issues that need addressing before finally choosing which SRAM to use.

- The chip enable input, when taken inactive, must disable all the other inputs to the SRAM. This allows inputs to the external SRAMs to be treated as "Don't Care" once  $V_{CC}$  falls below  $V_{PFD}(\min)$ .
- The SRAM should guarantee data retention when working at  $V_{CC} = 2.0$  volts.
- The chip-enable access time must be sufficient to meet the system needs, taking into account propagation delays on chip enable and output enable.

Most SRAMs specify a data retention current ( $I_{CCDR}$ ) at 3.0V. Manufacturers generally specify a typical condition for room temperature along with a worst case condition (generally at elevated temperatures). The system level requirements will determine the choice of which value to use. The data retention current value of the SRAMs can then be added to the  $I_{BAT}$  value of the SUPERVISOR to determine the total current requirements for data retention. The available battery capacity for the SNAPHAT of your choice can then be divided by this current to determine the data retention period (see the Capacity Consumption equation on page 6).

STMicroelectronics offers ultra low power 5V and 3V 128K x 8 SRAMs, as well as 512K x 8 SRAMs. These SRAMs are available for use with the SUPERVISOR devices in order to provide the longest possible data retention lifetimes. For example, the M48T201Y has an  $I_{BAT}$  value of 575 nA at 25°C, and 800 nA at 70°C. The M40Z300 has an  $I_{BAT}$  value of 5 nA at 25°C, and 100 nA at 70°C. Table 5 indicates typical Data Retention Lifetimes for the M40Z300 ZEROPOWER SUPERVISOR when it is used with a number of commercially available 1 Mbit and 4 Mbit SRAMs. Table 6 shows the same kind of information for the M48T201Y TIMEKEEPER SUPERVISOR.

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**Table 5. M40Z300 (with 120mAh SNAPHAT) Data Retention Life vs. SRAM Type**

Size	Product		I <sub>BAT</sub> (SRAM) (nA)		I <sub>BAT</sub> (Total) (nA)		Lifetime in Years <sup>(1)</sup>	
			25°C	70°C	25°C	70°C	25°C	70°C
1	ST	M68Z128	100	2000	105	2100	>>20	6.5
		M68Z128W <sup>2</sup>	100	2000	105	2100	>>20	6.5
	Samsung	KM68FV1000 <sup>2</sup>	100	1000	105	1100	> 20	12.5
		KM68FV1000L <sup>2</sup>	n/a	5000	n/a	5100	n/a	2.7
		KM681000CL-L	1000	10 000	1005	10100	14.8	1.4
		KM681000CL	1000	20 000	1005	20100	14.8	0.7
	Cypress	CY62128VL <sup>2</sup>	500	10 000	505	10100	> 20	1.4
		CY62128LL	600	10 000	605	10100	> 20	1.4
		CY62128L	600	100 000	605	100100	> 20	0.1
	Hitachi	HM628128BSL	1000	15 000	1005	15100	14.8	0.9
HM628128BL		1000	50 000	1005	50100	14.8	0.3	
4	ST	M68Z512	400	10,000	405	10,100	>>20	1.4
		M68Z512W <sup>2</sup>	400	10,000	405	10,100	>>20	1.4
	Mitsubishi	M5M5408ALL	400	10 000	405	10100	> 20	1.4
	Samsung	KM68V4000B <sup>2</sup>	500	15 000	505	15100	> 20	0.9
		KM684000BL-L	n/a	15 000	n/a	15100	n/a	0.9
		KM684000BL	n/a	50 000	n/a	50100	n/a	0.3
	Hitachi	HM628512B	n/a	20 000	n/a	20100	n/a	0.7

Note: 1. According to the respective manufacturer's data sheets, at the time of writing  
2. 3 V device

**Table 6. M48T201Y (Commercial - 1 with 120mAh SNAPHAT) Data Retention Life vs. SRAM Type**

Size	Product		I <sub>BAT</sub> (SRAM) (nA)		I <sub>BAT</sub> (Total) (nA)		Lifetime in Years <sup>(1)</sup>	
			25°C	70°C	25°C	70°C	25°C	70°C
1	ST	M68Z128	100	2000	675	2800	>20.3	4.9
		M68Z128W <sup>2</sup>	100	2000	675	2800	>20.3	4.9
	Samsung	KM68FV1000 <sup>2</sup>	100	1000	675	1800	> 20	7.6
		KM68FV1000L <sup>2</sup>	n/a	5000	n/a	5800	n/a	2.4
		KM681000CL-L	1000	10 000	1575	10800	9.4	1.3
		KM681000CL	1000	20 000	1575	20800	9.4	0.7
	Cypress	CY62128VL <sup>2</sup>	500	10 000	1075	10800	13.8	1.3
		CY62128LL	600	10 000	1175	10800	12.6	1.3
		CY62128L	600	100 000	1175	100800	12.6	0.9
	Hitachi	HM628128BSL	1000	15 000	1575	15800	9.4	0.9
HM628128BL		1000	50 000	1575	50800	9.4	0.3	
4	ST	M68Z512	400	10,000	975	10,800	>14.0	1.3
		M68Z512W <sup>2</sup>	400	10,000	975	10,800	>14.0	1.3
	Mitsubishi	M5M5408ALL	400	10 000	975	10800	15.2	1.3
	Samsung	KM68V4000B <sup>2</sup>	500	15 000	1075	15800	13.8	0.9
		KM684000BL-L	n/a	15 000	n/a	15800	n/a	0.9
		KM684000BL	n/a	50 000	n/a	50800	n/a	0.3
	Hitachi	HM628512B	n/a	20 000	n/a	20800	n/a	0.7

Note: 1. According to the respective manufacturer's data sheets, at the time of writing  
2. 3 V device

**INDUSTRIAL TEMPERATURE DEVICES**

Due to ever increasing requirements for portability and operation under extreme environmental conditions, Industrial Temperature versions (–40 to +85°C) of the M48Z02, M48Z58, M48Z35, M48T59, M48T35, M48T37, M48T201, and all of our Serial RTC devices have been introduced. This expanded operating range allows these products to perform under more extreme temperatures for applications such as: cell phone base stations; pay-phones; portable equipment; land, water and aircraft instrumentation; and industrial control equipment. These products are indicated by the digit '6' at the end of the sales-type. The Industrial Temperature TIMEKEEPER® SNAPHAT® top is also designated by the suffix "6." Predicted Data Retention lifetimes are listed in Appendices B and C.

**U.L. RECOGNITION AND RECYCLING**

While providing innovative, leading edge products, STMicroelectronics remains committed to safety: including its products, its customers, and the environment. Each device contains reverse-charge protection circuitry, and uses safe Lithium MonoFluoride batteries. All ZEROPOWER® and TIMEKEEPER components are recognized by Underwriter's Laboratory under file number E89556, and are compliant to the LL-94-VO flammability rating.

The unique SNAPHAT packaging consists of a 330mil SOIC device and a separate, "snap-on" SNAPHAT, which includes both the lithium power cell, and in the case of TIMEKEEPER product, a high accuracy crystal. The SNAPHAT is removable and can be replaced, providing the added benefit of proper disposal or recycling that has not been available before with NVRAMs. Various companies offer recycling and safe disposal of scrap lithium cells.

**SUMMARY**

Battery life and Data Retention for ZEROPOWER and TIMEKEEPER products is primarily a function of two factors, Capacity Consumption and Storage Life of the lithium button cell battery. Due to the fact that Storage Life (caused by electrolyte evaporation) has little effect at temperatures below 60°C, the Data Retention of most applications will be dependent upon the  $I_{CCDR}$  of the SRAM being backed-up, and the duty cycle of  $V_{CC}$ . This allows a fairly simple calculation (as developed on page 6) to be used to determine the lifetime.

All ST ZEROPOWER products are able to offer at least a 10 year data retention life, typically at 40°C. This may be increased by reducing the temperature, increasing the  $V_{CC}$  duty cycle, or in the case of the surface mount SNAPHAT products, using the larger 120mAh SNAPHAT top.

For the TIMEKEEPER family, battery lifetimes are also affected by the percentage of time the oscillator is in operation. General rules of thumb include:

- Devices fabricated in 6T technologies (M48Z08, M48Z18, M48T08, and M48T18) offer 10 years of continuous operation across the temperature range.
- Commercial devices fabricated in 4T technologies provide 7 years of continuous operation at 20°C using the 48mAh M4T28-BR12SH SNAPHAT top, and typically greater than 15.8 years with the 120mAh M4T32-BR12SH SNAPHAT top.

The ZEROPOWER and TIMEKEEPER SUPERVISOR families allow the user to purchase commodity SRAMs at best available market price. But the overall Data Retention Life will be determined by the  $I_{CCDR}$  of the SRAM selected.

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### APPENDICES

**Note:** for the purposes of these Appendices, the symbol ">>" means, "... much greater than..."

### APPENDIX A. PRODUCT DATA

**Table 7. Data for ZEROPOWER® and TIMEKEEPER® Devices**

Device	Process Technology	SRAM Cell	Battery Type		I <sub>BAT</sub> (T = 20°C) (nA)	Typical Data Retention Lifetime <sup>(2)</sup> (years)
			SNAPHAT <sup>(1)</sup>	CAPHAT		
M48Z02, M48Z12	0.6 μm, HCMOS4PZ	4T	n/a	BR1225	9	10
M48Z08, M48Z18	1.2 μm, HCMOS3	6T	BR1225	BR1225	23	10
M48Z58, M48Z58Y	0.6 μm, HCMOS4PZ	4T	BR1225	BR1225	37	10
M48Z35, M48Z35Y	0.6 μm, HCMOS4PZ	4T	BR1225	BR1225	148	10
M48T02, M48T12	0.6 μm, HCMOS4PZ	4T	n/a	BR1632	506	10
M48T08, M48T18	1.2 μm, HCMOS3	6T	BR1225	BR1225	480	10
M48T58, M48T58Y	0.6 μm, HCMOS4PZ	4T	BR1225	BR1225	535	7
M48T59, M48T59Y	0.6 μm, HCMOS4PZ	4T	BR1225	BR1225	535	7
M48T559Y	0.6 μm, HCMOS4PZ	4T	BR1225	n/a	535	7
M48T35, M48T35Y	0.6 μm, HCMOS4PZ	4T	BR1225	BR1632	646	7/10
M48T37Y	0.6 μm, HCMOS4PZ	4T	BR1225	n/a	646	7

Note: 1. The larger capacity BR1632 (120 mAh) battery is also available in the SNAPHAT package.

2. The Data Retention Lifetime can be significantly increased by using the SNAPHAT (ZEROPOWER or TIMEKEEPER, as appropriate) with the higher capacity BR1632 battery.

**Table 8. Data from Hybrid/Module Devices (V<sub>CC</sub> duty cycle = 0%)**

Device	Specification at T = 25°C (years)	Experimental Conditions (years)		
		0°C	25°C	70°C
M48Z128, M48Z128Y, M48Z128V	10	>> 20	> 20	2.3
M48Z129Y, M48Z129V	10	>> 20	> 20	2.3
M48Z512A, M48Z512AY, M48Z512AV	10	>> 20	> 20	6.0
M48Z2M1, M48Z2M1Y	10	> 20	> 20	3.1
M48T128Y, M48T128V	10	> 20	16.6	2.0
M48T129Y, M48T129V	10	> 20	16.6	2.0
M48T512Y, M48T512V	10	> 20	19.4	4.8
M48T513Y, M48T513V	10	> 20	19.4	4.8



**APPENDIX B. ZEROPOWER PRODUCTS**

The tables in this appendix use the terms “typical” and “worst case” to indicate the “mean value at the given temperature” and “mean value plus maximum expected deviation at the given temperature.”

**Table 9. Data from M48Z02 and M48Z12 Devices (available only in CAPHAT - BR1225, 48mAh)**

Temperature (°C)	VCC Duty Cycle = 0%		VCC Duty Cycle = 100%, Shelf Life (years)
	Typical (years)	Worst Case (years)	
0	>> 20	>> 20	>> 20
10	>> 20	>> 20	>> 20
20	>> 20	>> 20	>> 20
25	>> 20	>> 20	>> 20
30	>> 20	>> 20	>> 20
40	>> 20	>> 20	>> 20
50	>> 20	> 20	>> 20
60	> 20	> 20	> 20
70	11.0	11.0	11.0

**Table 10. Data from M48Z02 Device (Industrial - 6; available only in CAPHAT, BR1632, 120mAh)**

Temperature (°C)	VCC Duty Cycle = 0%		VCC Duty Cycle = 100%, Shelf Life (years)
	Typical (years)	Worst Case (years)	
-40	>> 20	>> 20	>> 20
-30	>> 20	>> 20	>> 20
-20	>> 20	>> 20	>> 20
-10	>> 20	>> 20	>> 20
0	>> 20	>> 20	>> 20
10	>> 20	>> 20	>> 20
20	>> 20	>> 20	>> 20
25	>> 20	>> 20	>> 20
30	>> 20	>> 20	>> 20
40	>> 20	>> 20	>> 20
50	>> 20	> 20	>> 20
60	> 20	> 20	> 20
70	11.0	11.0	11.0
80	4.3	4.3	4.3
85	2.7	2.7	2.7

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Table 11. Data from M48Z08 and M48Z18 Devices

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)	SNAPHAT <sup>(2)</sup> (BR1632, 120mAh)	V <sub>CC</sub> Duty Cycle = 100%, Shelf Life <sup>(1)</sup> (years)
	V <sub>CC</sub> Duty Cycle = 0%, Typical (years)		
0	>> 20	>> 20	>> 20
10	>> 20	>> 20	>> 20
20	>> 20	>> 20	>> 20
25	>> 20	>> 20	>> 20
30	>> 20	>> 20	>> 20
40	>> 20	>> 20	>> 20
50	>> 20	>> 20	>> 20
60	> 20	> 20	> 20
70	11.0	11.0	11.0

Note: 1. The worst case lifetime at 25 °C is 14 years.

2. Only the M48Z18 is available in SNAPHAT.

Table 12. Data from M48Z58 and M48Z58Y Devices (Commercial - 1)

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)		SNAPHAT <sup>(1)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%				
	Typical (years)	Worst Case (years)	Typical (years)	Worst Case (years)	
0	>> 20	>> 20	>> 20	>> 20	>> 20
10	>> 20	>> 20	>> 20	>> 20	>> 20
20	>> 20	>> 20	>> 20	>> 20	>> 20
25	>> 20	>> 20	>> 20	>> 20	>> 20
30	>> 20	>> 20	>> 20	>> 20	>> 20
40	>> 20	> 20	>> 20	>> 20	>> 20
50	> 20	12.4	>> 20	> 20	>> 20
60	18.1	7.4	> 20	18.5	> 20
70	10.7	4.4	11.0	11.0	11.0

Note: 1. Only the M48Z58Y is available in SNAPHAT.

Table 13. Data from M48Z58Y Device (Industrial - 6)

Temperature (°C)	SNAPHAT <sup>(1)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%		
	Typical (years)	Worst Case (years)	
-40	>> 20	>> 20	>> 20
-30	>> 20	>> 20	>> 20
-20	>> 20	>> 20	>> 20
-10	>> 20	>> 20	>> 20
0	>> 20	>> 20	>> 20
10	>> 20	>> 20	>> 20
20	>> 20	>> 20	>> 20
25	>> 20	>> 20	>> 20
30	>> 20	>> 20	>> 20
40	>> 20	>> 20	>> 20
50	> 20	12.5	>> 20
60	15.0	7.5	> 20
70	8.9	4.4	11.0
80	4.3	2.6	4.3
85	2.7	2.0	2.7

Note: 1. Only the M48Z58Y is available in SNAPHAT.

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**Table 14. Data from M48Z35 and M48Z35Y Devices (Commercial - 1)**

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)		SNAPHAT <sup>(1)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%				
	Typical (years)	Worst Case (years)	Typical (years)	Worst Case (years)	
0	>> 20	>> 20	>> 20	>> 20	>> 20
10	>> 20	> 20	>> 20	>> 20	>> 20
20	> 20	14.8	>> 20	> 20	>> 20
25	> 20	11.4	>> 20	> 20	>> 20
30	> 20	8.8	>> 20	> 20	>> 20
40	12.9	5.2	> 20	13.1	>> 20
50	7.6	3.1	19.1	7.8	>> 20
60	4.5	1.9	11.3	4.6	> 20
70	2.7	1.1	6.7	2.8	11.0

Note: 1. Only the M48Z35Y is available in SNAPHAT.

**Table 15. Data from M48Z35Y Device (Industrial - 6)**

Temperature (°C)	SNAPHAT <sup>(1)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%		
	Typical (years)	Worst Case (years)	
-40	>> 20	>> 20	>> 20
-30	>> 20	>> 20	>> 20
-20	>> 20	>> 20	>> 20
-10	>> 20	>> 20	>> 20
0	>> 20	>> 20	>> 20
10	>> 20	> 20	>> 20
20	> 20	14.7	>> 20
25	> 20	11.4	>> 20
30	17.6	8.6	>> 20
40	10.5	5.2	>> 20
50	6.2	3.1	>> 20
60	3.7	1.9	> 20
70	2.2	1.1	11.0
80	1.3	0.6	4.3
85	1.0	0.5	2.7

Note: 1. Only the M48Z35Y is available in SNAPHAT.

## APPENDIX C. TIMEKEEPER® PRODUCTS

Table 16. Data from M48T86 Device

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)	SNAPHAT (BR1632, 120mAh)	V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%, Typical (years)		
0	13.4	> 20	>> 20
10	13.2	> 20	>> 20
20	13.1	> 20	>> 20
25	13.0	> 20	>> 20
30	13.0	> 20	>> 20
40	12.9	> 20	>> 20
50	12.8	> 20	>> 20
60	12.7	> 20	> 20
70	11.0	11.0	11.0

Table 17. Data from M48T02 and M48T12 Devices (available only in CAPHAT - BR1632, 120mAh)

Temperature (°C)	V <sub>CC</sub> Duty Cycle = 0%		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	Typical (years)	Worst Case (years)	
0	> 20	> 20	>> 20
10	> 20	> 20	>> 20
20	> 20	> 20	>> 20
25	> 20	> 20	>> 20
30	> 20	> 20	>> 20
40	> 20	> 20	>> 20
50	> 20	18.9	>> 20
60	> 20	16.8	> 20
70	11.0	11.0	11.0

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Table 18. Data from M48T08 and M48T18 Devices

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)	SNAPHAT <sup>(2)</sup> (BR1632, 120mAh)	V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years) <sup>(1)</sup>
	V <sub>CC</sub> Duty Cycle = 0%, Typical (years)		
0	10.9	> 20	>> 20
10	11.3	> 20	>> 20
20	11.4	> 20	>> 20
25	11.4	> 20	>> 20
30	11.4	> 20	>> 20
40	11.4	> 20	>> 20
50	11.5	> 20	>> 20
60	11.8	> 20	> 20
70	11.0	11.0	11.0

Note: 1. The worst case lifetime at 25 °C is greater than 10 years.  
2. Only the M48T18 is available in SNAPHAT.

Table 19. Data from M48T58, M48T58Y, M48T59, M48T59Y and M48T559Y Devices

Temperature (°C)	CAPHAT or SNAPHAT (BR1225, 48mAh)		SNAPHAT <sup>(1,2)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%				
	Typical (years)	Worst Case (years)	Typical (years)	Worst Case (years)	
0	12.3	10.6	> 20	> 20	>> 20
10	11.2	9.5	> 20	> 20	>> 20
20	10.2	8.4	> 20	> 20	>> 20
25	9.8	7.9	> 20	19.8	>> 20
30	9.3	7.4	> 20	18.5	>> 20
40	8.3	6.4	> 20	15.9	>> 20
50	7.3	5.2	18.2	13.0	>> 20
60	6.1	4.0	15.2	10.0	> 20
70	4.8	2.9	11.0	7.2	11.0

Note: 1. The M48T559Y is available in SNAPHAT only.  
2. Only the M48T58Y and M48T59Y are available in SNAPHAT.

Table 20. Data from M48T59Y Industrial Temperature (MH6) Devices

Temperature (°C)	SNAPHAT (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%		
	Typical (years)	Worst Case (years)	
-40	> 20	> 20	>> 20
-30	> 20	> 20	>> 20
-20	> 20	> 20	>> 20
-10	> 20	> 20	>> 20
0	> 20	> 20	>> 20
10	> 20	18.5	>> 20
20	18.2	15.7	>> 20
25	17.0	14.2	>> 20
30	15.9	12.7	>> 20
40	13.4	10.0	>> 20
50	10.8	7.4	>> 20
60	8.2	5.2	> 20
70	5.9	3.5	11.0
80	4.0	2.2	4.3
85	2.7	1.8	2.7

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**Table 21. Data from M48T35, M48T35Y, M48T37Y and M48T37V Devices**

Temperature (°C)	SNAPHAT (BR1225, 48mAh)		CAPHAT or SNAPHAT <sup>(1,2)</sup> (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%				
	Typical (years)	Worst Case (years)	Typical (years)	Worst Case (years)	
0	11.3	8.9	> 20	> 20	>> 20
10	9.9	7.1	> 20	18.5	>> 20
20	8.5	5.9	> 20	14.7	>> 20
25	7.8	5.2	19.5	13.0	>> 20
30	7.1	4.5	17.7	11.3	>> 20
40	5.6	3.3	14.1	8.3	>> 20
50	4.3	2.3	10.6	5.7	>> 20
60	3.0	1.5	7.6	3.8	> 20
70	2.1	1.0	5.1	2.4	11.0

Note: 1. The M48T37Y and M48T37V are available in SNAPHAT only.  
2. Only the M48T35Y is available in SNAPHAT.

**Table 22. Data from M48T35Y, M48T37Y & M48T37V Industrial Temperature (MH6) Devices**

Temperature (°C)	SNAPHAT (BR1632, 120mAh)		V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)
	V <sub>CC</sub> Duty Cycle = 0%		
	Typical (years)	Worst Case (years)	
-40	> 20	> 20	>> 20
-30	> 20	> 20	>> 20
-20	> 20	> 20	>> 20
-10	> 20	18.9	>> 20
0	18.9	15.3	>> 20
10	15.7	11.8	>> 20
20	12.5	8.7	>> 20
25	10.9	7.4	>> 20
30	9.5	6.0	>> 20
40	6.9	4.1	>> 20
50	4.7	2.7	>> 20
60	3.1	1.7	> 20
70	2.0	1.0	11.0
80	1.2	0.6	4.3
85	1.0	0.5	2.7



## APPENDIX D. SERIAL RTC PRODUCTS

Table 23. Data from M41T56, M41T94, M41ST84, M41ST85, and M41T315 Industrial Temperature (MH6) Devices

Temperature (°C)	SNAPHAT (BR1632, 120mAh)	
	V <sub>CC</sub> Duty Cycle = 0%	
	Typical (years)	
	V <sub>CC</sub> Duty Cycle = 100%, Shelf Life (years)	
-40	> 20	>> 20
-30	> 20	>> 20
-20	> 20	>> 20
-10	> 20	>> 20
0	> 20	>> 20
10	> 20	>> 20
20	> 20	>> 20
25	> 20	>> 20
30	> 20	>> 20
40	> 20	>> 20
50	> 20	>> 20
60	> 20	> 20
70	11.0	11.0
80	4.3	4.3
85	2.7	2.7

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**Table 24. Data from M41T00, M41T11, and M41T81 Industrial Temperature (MH6) Devices**

Temperature (°C)	SNAPHAT (BR1632, 120mAh)	
	V <sub>CC</sub> Duty Cycle = 0%	
	Typical (years)	
-40	> 20	>> 20
-30	> 20	>> 20
-20	> 20	>> 20
-10	> 20	>> 20
0	> 20	>> 20
10	> 20	>> 20
20	> 20	>> 20
25	> 20	>> 20
30	> 20	>> 20
40	> 20	>> 20
50	> 20	>> 20
60	> 20	> 20
70	11.0	11.0
80	4.3	4.3
85	2.7	2.7

**REVISION HISTORY****Table 25. Document Revision History**

Date	Description of Revision
13-Oct-1998	Document written
14-Dec-1998	1st Edition of ZEROPOWER and TIMEKEEPER Application Note Book
07-Mar-2000	Data changed from that of 49mAh and 130mAh batteries to that of 48mAh and 120mAh batteries
25-Apr-2000	Controllers renamed as SUPERVISORS
26-Jun-2000	M48T35 typ data retention lifetime changed to 7/10 years (Tab-7 on p15)
05/08/01	Reformatted, text, graphics, values updated (figures 6, 7, 8, 9; tables 3, 5, 6, 7, 10, 16, 19, 21, 20, 22, 23, 24)
05/15/01	Change trend colors to black (Figure 6, 7, 8, 9)

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### CONTACT INFORMATION

If you have any questions or suggestions concerning the matters raised in this document, please send them to the following electronic mail addresses:

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